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THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 09/976,490
Filed: October 12, 2001
Inventor(s):
Michael C. Dorsey

Examiner: Trimmings, J.
Group/Art Unit: 2133
Atty. Dkt. No: 5681-55200

Title: ASIC BIST
CONTROLLER
EMPLOYING MULTIPLE
CLOCK DOMAINS

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date indicated below.

Erik A. Heter
Printed Name
May 27, 2004
Signature Date

AMENDMENT; RESPONSE TO OFFICE ACTION OF
February 27, 2004

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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Technology Center 2100

Dear Sir:

This paper is submitted in response to the Office Action of February 27, 2004, to further highlight why the application is in condition for allowance.

Please amend the case as listed below. Amendments to the specification begin on page 2. A listing of the claims, including any amendments, begins on page 5. Remarks begin on page 11.